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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeffrey S. Mailloux et al.

Title: ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION

Docket No.: 303.623US4

Serial No.: 08/984,563

Filed: December 3, 1997

Due Date: October 25, 2000

Examiner: Hong Kim

Group Art Unit: 2751

Commissioner for Patents
Washington, D.C. 20231



We are transmitting herewith the following attached items (as indicated with an "X"):

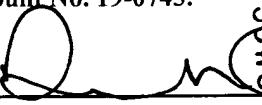
- A return postcard.
- An Amendment and Response (4 Pages).
- Petition for Extension of Time (1 pg.)
- A check in the amount of \$890.00 to cover the Extension of Time Fee.
- A check in the amount of \$80.00 to cover the fee for additional claims as calculated below.

If an additional fee is required due to changes to the claims, the fee has been calculated as follows:

CLAIMS AS AMENDED						
	(1) Claims Remaining After Amendment		(2) Highest Number Previously Paid For	(3) Present Extra	Rate	Fee
TOTAL CLAIMS	20	-	20		x 18 =	\$0.00
INDEPENDENT CLAIMS	9	-	8	1	x 80 =	\$80.00
[] MULTIPLE DEPENDENT CLAIMS PRESENTED						\$0.00
TOTAL						\$80.00

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 20 day of October, 2000.

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(GENERAL)

S/N 08/984,563

PATENT

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AMENDMENT AND RESPONSE

Assistant Commissioner for Patents
Washington, D.C. 20231



Applicants have reviewed the Office Action mailed on April 25, 2000. Please amend the above-identified patent application as follows.

IN THE CLAIMS

Please add the following new claims:

70. (New) A method for accessing an asynchronous DRAM, comprising:
switching between a burst and a pipelined EDO mode in the absence of a WCR cycle;
selecting an external address data path;
instructing the asynchronous DRAM a desired memory operation; and
performing the desired memory operation until terminated.

71. (New) The method of claim 70, wherein selecting includes selecting the external address data path from a buffer when the act of switching switches to the burst EDO mode.

72. (New) The method of claim 70, wherein instructing includes instructing the asynchronous DRAM the desired memory operation, wherein the desired memory operation is selected from a group consisting of a read operation and a write operation.

73. (New) The method of claim 70, wherein performing includes obtaining an external column address so as to perform the desired memory operation, and wherein performing includes iterating the act of obtaining the external column address until terminated.

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page 18
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17